




Performance, Sizing, Tips & Tools: The Latest and the Greatest for i5/OS

Common Luxembourg
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Performance, Sizing, Tips and Tools

What is Performance?

- Definition:
 - Performance: The degree to which a system or component accomplishes its designated functions within given constraints, such as speed, accuracy, or memory usage [IEEE-610.12]
- In general:
 - Predictability and timeliness of response is what performance is about
 - A “faster” system is not always enough; for example, a real-time system requires repeatable results
- A quote from the Institute of Computer Capacity Management (ICCM):
 - “A manager’s goal should always be to strike the right balance between system function, processing costs, people costs, and performance. This is why the technical aspects of performance can never be entirely divorced from organizational politics.”

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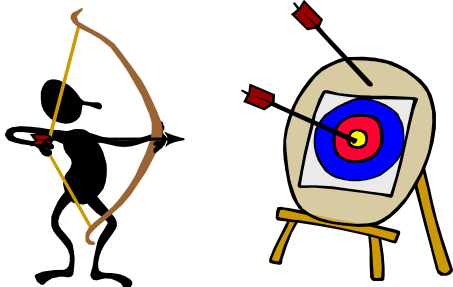


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Successful Systems have at least Three Characteristics

- The right function
- Operational integrity
- Acceptable performance

All three are necessary or the solution would fail.



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An Example of a Throughput Disaster

- Application
 - Monthly production schedule creation
 - Target: 16 million items to be planned in an 8-hour batch window
- Testing
 - Unit and integration tests: Function runs correctly.
 - System test: Two weeks before going live, the system was still running after 48 hours.
 - Progress analysis indicated the likely time to completion: **154 years**.
- Resolution
 - System went live four months late.
 - The project incurred significant extra costs.




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Understand the Requirements

- A simple performance statement, such as “a response time of less than one second,” is insufficient. It should always contain several components, such as:
 - What is the performance requirement? (Response time? Transaction time?)
 - When is it to be delivered (in defined service hours?)
 - Where is it to be measured? (On which systems, using which tools?)
 - For what workload is it to be valid? (Are test transactions available?)
 - For which users or systems? (And how many?)
 - How can you define the percentile to achieve the given value? (ninety-fifth percentile? average?)
- Remember: In the e-business world, response time can equal competitive advantage



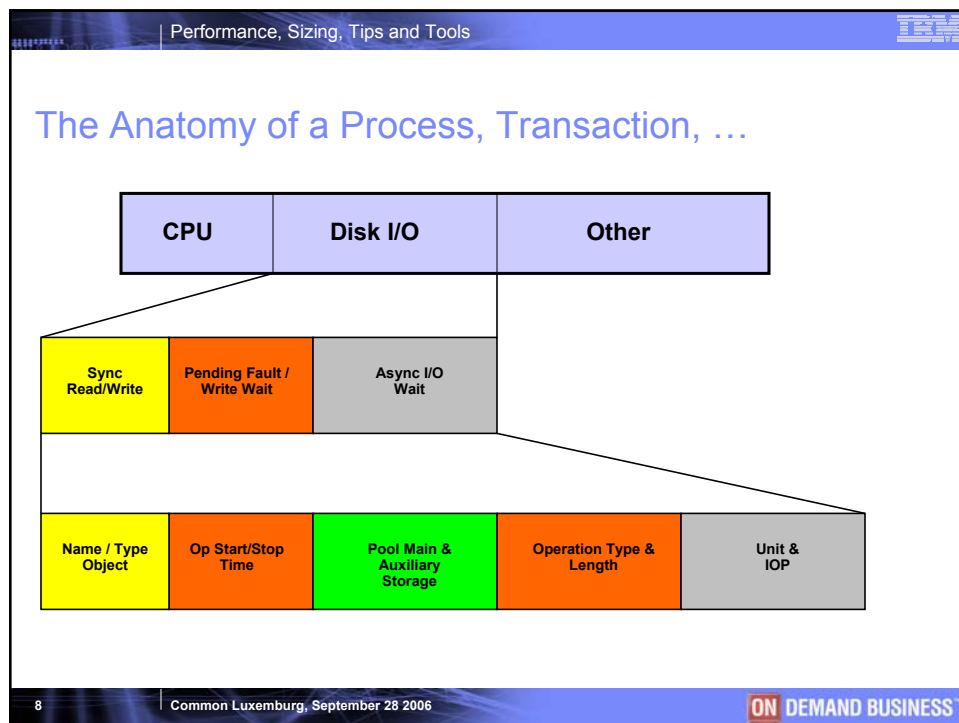
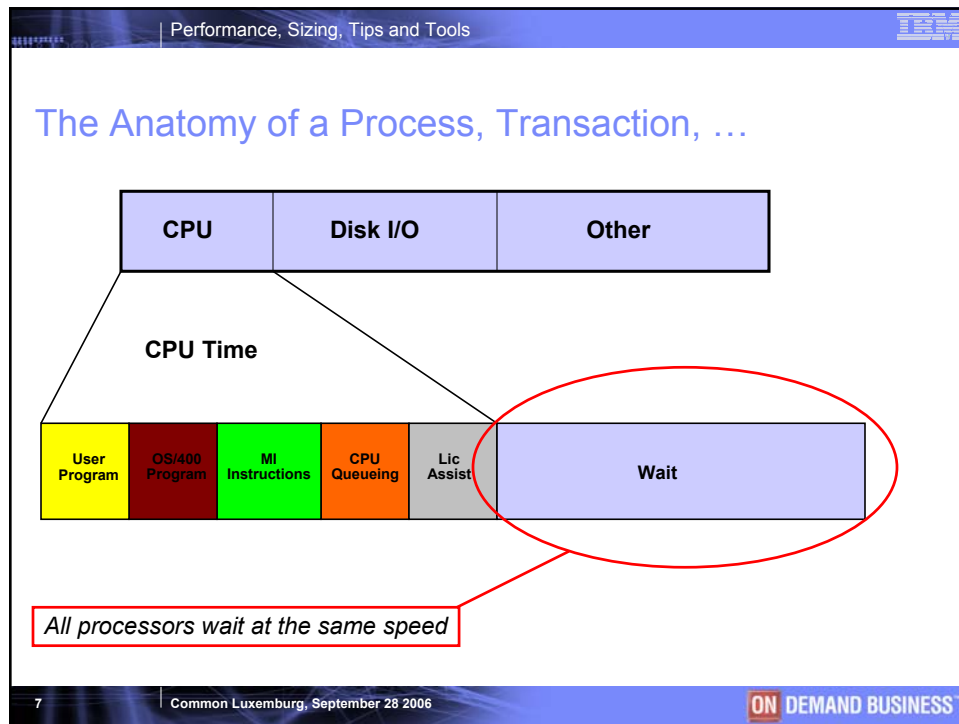
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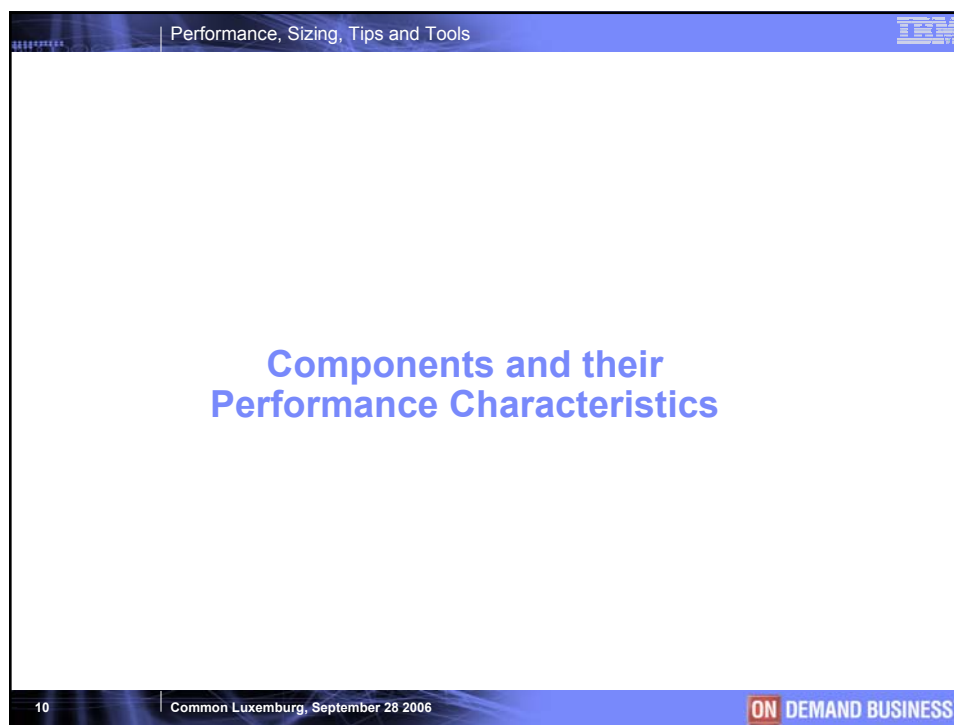
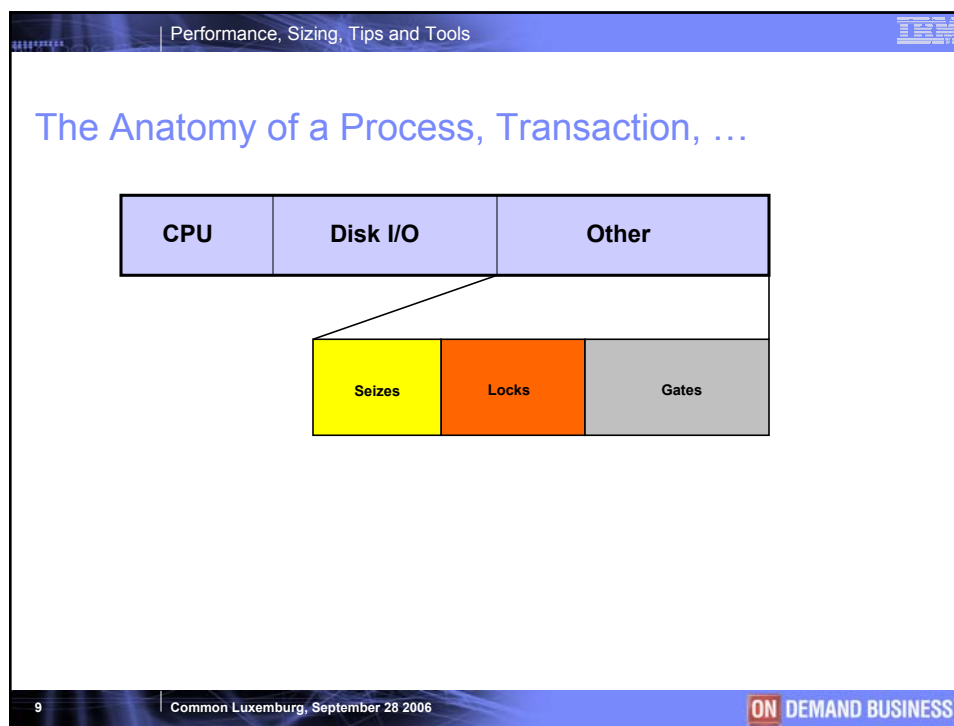
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Remember that the Users of the System are Human

- People's senses are analog not digital.
 - For example, water temperature - freezing, cold, warm, hot, boiling
- Interaction perception:
 - People treat computers as people too.
 - The steps in people's sense of time are as follows:
 - Instantaneous = below 0.2 seconds (for example, typing)
 - Immediate = Sub-second (like-minded colleague / aggressive stranger)
 - Fast = under 2 seconds (frequent or standard request)
 - Normal = 3 seconds (typical conversation)
 - Extended = 5 seconds (slow speaker - ideally needs to make sound in the long pauses to show they are still speaking)
 - Too long = about 17 seconds (Other party gives up, saying “Come and tell me when you have an answer”)
- Performance does not mean blazing speed. It means an acceptable and predictable level of responsiveness. Aim for consistency!

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What's out there?

- Processors – can somebody define it?
- Memory – does it scale? How much do I need?
- Disk units – how do they behave?

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A Processor can be Lots of Things ...

1995 Cobra Muskies Apache Northstar Pulsar IStar SStar POWER4 POWER5 POWER5+ POWER6

2004 2005-6 Future

Lotus WordPro 96

Microsoft Word

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What is a Processor?

We like to say:

**We have the fastest
4-way in the industry!**

But what is a 4-way?

The answer to this seemingly simple question, is a little like.....

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intel.

A Three

AMD

Ring

Sun
microsystems

Circus

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Why the Confusion?

IBM has Power4 and Power5 processor chips that contain 2 cores on the chip – We naturally called this 2 processors, because each core acts largely independently of the other.

Of course, we also have SMT, which runs two processor threads on each core, making it look like each core is two processors to the software.....

Intel figured out that IBM's 1999 HMT (hardware multithreading) on Power3 was a good thing, and came out with hyperthreading, that allows multiple processor threads to interleave on a single core. Recently, they have come out with Dual-core "Processors"

With both dual cores and hyperthreading, they have the ability to match words with Power5 - - - as soon as they start shipping it on more than workstations

Sun has introduced UltraSPARC IV with Chip Multithreading

Which means they have dual (independent) core chips that they call single processors that have no actual multithreading in the same sense that IBM and Intel have – not unlike Power4 but very unlike Power5

AMD has introduced "dual-core processors" and has begun to talk about "multi-core processors"

Although their verbiage still sometimes says things like "With multi-core technology, your computing load can be spread across two or more processors"

(<http://multicore.amd.com/WhatIsMC/>)

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Consider the 4-way "Processor"

Technology	Chips	Processors	Cores	Threads
IStar/SStar (w/HMT)	4	4	4	8
Power4	2	4	4	4
Power5 (w/SMT)	2	4	4	8
Intel-single core (w/hyperthread)	4	4	4	8
Intel-dual core (w/HT)	4	4	8	16
AMD-single core	4	4	4	4
AMD-dual core	4	4	8	8
Sun SPARC3	4	4	4	4
Sun SPARC4	4	4	8	8
Sun Niagara	4	4	32	128

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What IBM says

IBM - z	A processor is a core
IBM - i	A processor is a core
IBM - x	A processor is whatever our processor providers proclaim it to be. We cannot sell our solutions using different vocabulary than others using the same technology
IBM - p	A processor is a core, but we should really call it a "processor-core"
IBM - SWG	<p>We would like to say a processor is a core, but</p> <ul style="list-style-type: none"> -- may need to follow definitions of processor manufacturers -- agree to use processor as a chip for two OpenPower systems -- not all cores are created equal -- we have a challenge getting equal payment for equal performance, when processors and cores all perform differently
Mostly	We will deal with cores as the primary units of computer processing.

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Performance Analysis: What must be done?

- Be careful where you step
- Know your architecture and your tools
 - Some tools will report multithreading as individual processors, even though the threads are not using independent resources
 - Tools that report processor threads as processors may yield different analysis impressions depending on whether multithreading is active or not
 - Most tools will report cores as processors, even though the product is advertised with processor chips
- Of course, virtualization of processors throws entirely different variables into the discussion

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Capacity Planning: What must be done?

- Be careful where you step
- For capacity planning – must pay attention to
 - Processor Cores
 - Processor Threads
 - And the relative strengths of these to deliver performance for a particular workload
- Don't predict response time (or batch run time) improvement based on rPerf or CPW comparisons between Power4 and Power5
 - These are reasonable indicators of commercial multitasking capacity, but they are NOT good predictors of single transactions, since Power5 results will include SMT

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Conclusion

- Capacity Planning now requires
 - The understanding of the unique components that make up a “processor”
 - The understanding of how a workload might use those components
 - The understanding of how a workload might change with a different set of components
- Software design (at least kernel, but probably middleware) now requires
 - The understanding of which components are available to use
 - Multiple optimization paths depending on this discovery
 - Possible just-in-time microcode loads of “on-demand accelerators”
- Sometimes
 - An application will directly use all the components
 - The kernel or firmware may “elect” to use some components on the application's behalf
 - The components won't be used
- And for processor performance analysis, we might just use ...

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Another Kind of Processor



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Settings that affect Processor Behavior

- Processor multithreading set at system level (QPRCMLTTSK system value)
 - Options are “Off” - “0” - , “On” - “1” - or “System controlled” - “2”
 - “System controlled” is valid for any hardware
 - Value change only takes effect after IPL
- When using partitioning, a number of virtual processors can be defined at the partition definition level:
 - Virtual processors are the whole number of concurrent operations which the operating system can utilize
 - ROT: Balance virtual processors to processor units:
 - If 1 processor or less, define 1 virtual processor
 - If 2 “ “ “ , define 2 virtual processors
 - Imbalance between virtual and real processors can slow down batch processing
- Performance Tools capture and report all aspects of processor capacity used

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Many more Settings that affect Processor Behavior

- Run priority attribute for jobs and system values that affect them:
 - Dynamically adjust job priorities within priority bands (QDYNPTYSCD)
 - Dynamically adjust job priorities for interactive jobs (QDYNPTYADJ): only valid on systems with # interactive and total processor capacity and requires QDYNPTYSCD to be on
- Time slice settings
- Activity settings (on memory pools; system-wide):
 - Threads versus jobs
- Job mixes in subsystems

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Data and Methodology for CPU Utilization

- QAPMSYSTEM table CPU calculation fields for:
 - Partition (System) Capacity Information
 - SYSCTA: Total CPU time (in milliseconds) configured (guaranteed capacity)
 - SYSPTU: Total CPU time (in milliseconds) used
 - SYSUTA: Available CPU time (in milliseconds) that could have been used
 - SYIFTA: 5250 OLTP CPU time (in milliseconds) configured/Interactive CPU time available capacity configured for use by the partition
 - SYIFUS: Interactive CPU time (in milliseconds) charged (used) to 5250-OLTP capacity
 - SYIFTE: Elapsed time (in milliseconds) when 5250 OLTP threshold was exceeded
- Shared Processor Pool Information
 - SYSPLA: Total CPU time (in milliseconds) in shared processor pool
 - SYSPLU: Total CPU time (in milliseconds) used by the shared processor pool partitions

Total Configured CPU Util % = (SYSPTU/SYSCTA) * 100
Total Available CPU Util % = (SYSPTU/SYSUTA) * 100

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Partition Sizing – CPU Utilization Considerations

- Dedicated processors = best performance
 - Example: Two 4 dedicated processor partitions yield more total CPW than single 8 processor partition
- For shared, micro partitioning best performance:
 - Assigned processors greater than or equal to 1
 - Virtual processors 2x of assigned processors if short CPU per transaction
 - Consider uncapped partitions, but...
- Less than expected partition performance, if
 - Less than one whole processor assigned to a partition and high CPU workload
 - Virtual processors 10x of assigned shared processors
 - Tasks run for longer than fraction of single processor. For example, a .6 processor and task runs for longer than 6 milliseconds
- Check: “Sizing IBM i5/OS Work on IBM System i5 Partitions” - SG24-6656

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Partition Sizing based on CPW

- Size your workload requirement – determine CPW required
 - For example 1.100
- Calculate the CPW per processor on the selected model
 - For example 520 = 3.300
- Determine multiplier for processor efficiency
 - $3.300/1.100 = 3$
- Micro partitioning efficiency reduction: you decide: multiply by 5% minimum, up to 40% maximum. The basic concept is that the smaller the micro-partition the higher the multiplication factor
 - Example: $3 * 0,05 = 0,15$
- Final partition processor unit size required
 - $1.100 * (1,15)/3.300 = \underline{0.38 \text{ processors}}$

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Capped versus Uncapped Partitioning

- Capped (Only option up to V5R2/POWER4)
 - Limited by the entitled capacity (Max 100%)
 - The entitled capacity is assured (.75 Processing Units = 7.5 MS)
 - Partition CANNOT use any unused capacity in the processor pool
 - CPU queuing algorithms work
- Uncapped May exceed the entitled capacity (>100% - based on entitled)
 - The entitled capacity is assured
 - Partition CAN use unused capacity in the processor pool
 - Upper limit of utilization determined by VIRTUAL PROCESSORS
 - Partition Weight Factor determines ratio of allocation of unused capacity – when more than one partition requests unused capacity. Be careful here, a weight of zero is actually a capped partition. Default of 128 for all partitions generally is not a good idea

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General Rules

- Catch 22:
 - Avoid ☒ → ● → ● → ● → ● → ● → ● → ● **Swapping** but swapping cannot be avoided
- If
 - You know your application and
 - You know your system then
 - Automatic performance adjustment could be an option you do not really want to follow unless you customize shared pool behavior
- Using other tools requires you to know the tool very well also
- Don't stack tools

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Rules-of-Thumb

- Machine pool set at about 10% of available storage – faulting should be less than 5 per second
- Limit the number of pools to a maximum of 8
- Base pool preferably used for subsystem management only:
 - Small amount of memory needed
 - Requires changes to all IBM shipped subsystems
- Calculate activity tool based on number of **threads** to run concurrently
- Expert cache is very useful for transactional and DB intensive work
- Avoid using QTSEPOOL to move interactive jobs to the base pool at timeslice end:
 - Verify why jobs are going to timeslice end
- More memory is always a good option but does not fix bad application design
- Power5 or Power5+ run reasonably well from 6 GB per processor on

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Tricks of the Trade

- Consider Journal Caching to limit the impact of journal wait time
 - Install option 42 of the OS
 - Set the JRNCACHE to *YES on the CHGJRN command
 - Commitment Control already uses some journal caching
- Consider Concurrent Write to optimize DB update or write time
 - CALL QDBENCWT '1' to enable or '0' to disable
 - Requires an IPL to be activated

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Topology

- Persistence of data flows from memory via I/O Processor to a disk controller (I/O Adapter) to a (set of) disk(s)
- Storage optimization done based on capacity used and number of I/Os per spindle
- Objects might be grouped for efficiency in ASPs, housed under a separate IOP/IOA
 - Journal receivers are prime candidates to do this if only deposits are done
 - Calculate efficiency of such a setup in the Component Report

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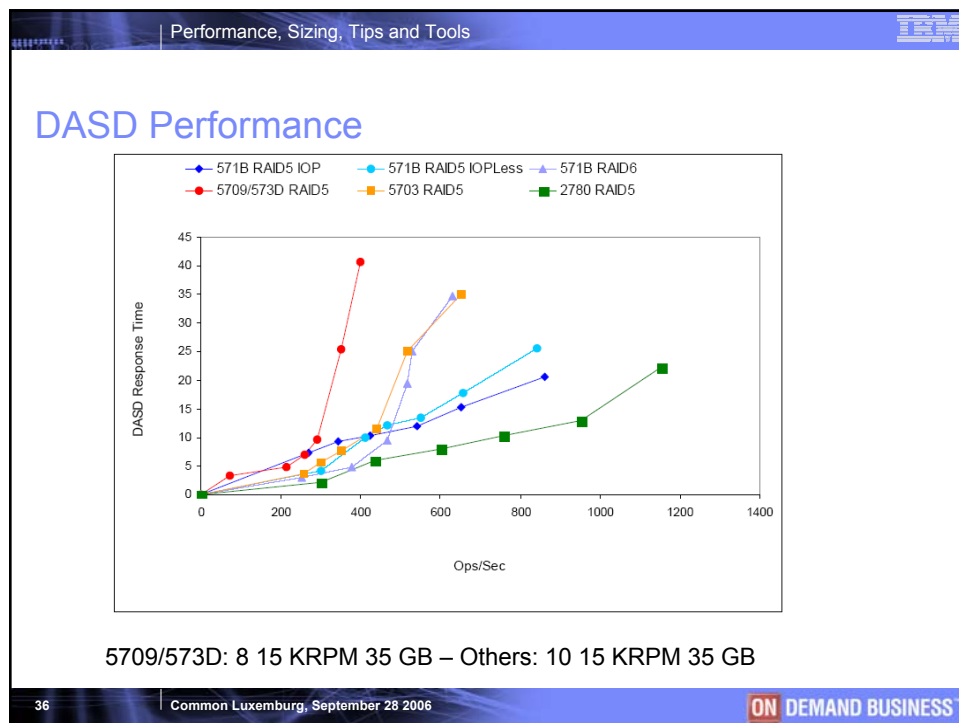
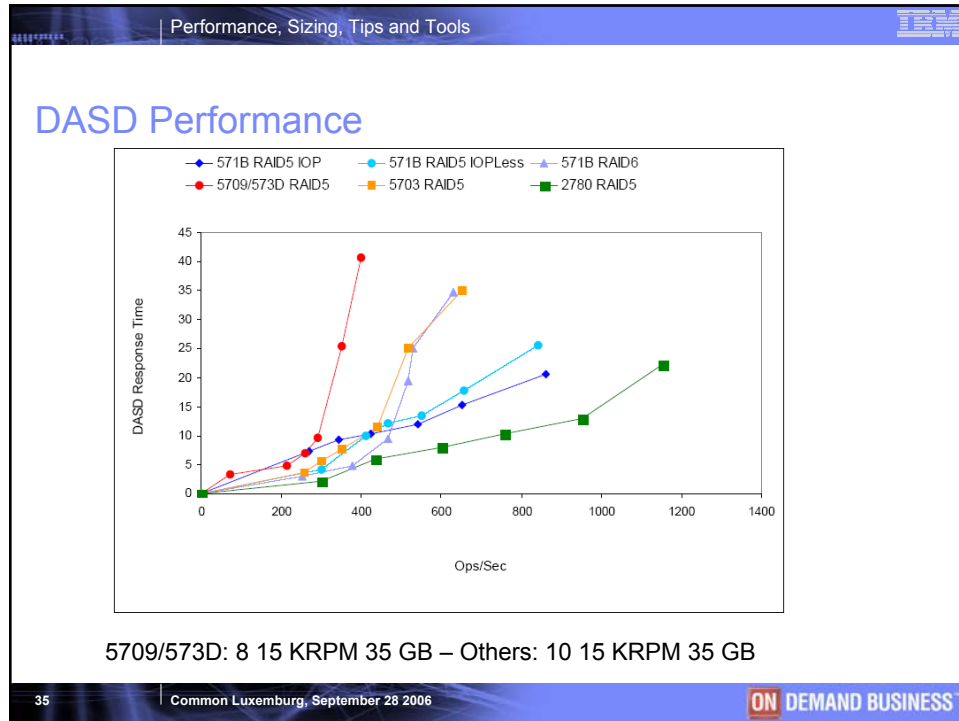
Disk Configuration – All Disk Types

- Limit the number of disk controllers per bus – check with the rules for high capacity I/O adapters
- Disk capacity needs to be calculated based on:
 - Storage capacity needed:
 - Do not exceed 80% capacity utilization
 - Temporary space can grow rapidly
 - Number of I/Os expected:
 - Don't load disks for more than 35% busy
 - Large blocks or sequential ops will provide a good yield
- Disk utilization can be improved with ASP balancing or disk reorganization
- Faster disks are ... faster
- Large memory on controllers or subsystems yields better performance but may cause data to be staged

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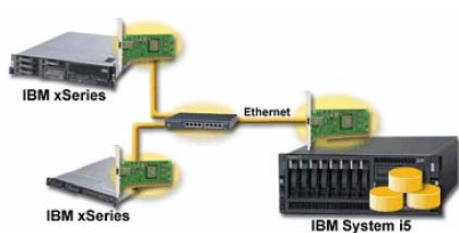
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System i as a Storage Server

- Providing support to store storage associated with other Operating Systems, especially Microsoft or Linux based
- New kid on the block: iSCSI support for xSeries or Blade Center



The diagram illustrates a storage architecture where two IBM xSeries servers are connected to an IBM System i5 server via an Ethernet network. The xSeries servers are shown on the left, and the System i5 server is on the right. The connection is labeled 'Ethernet'.

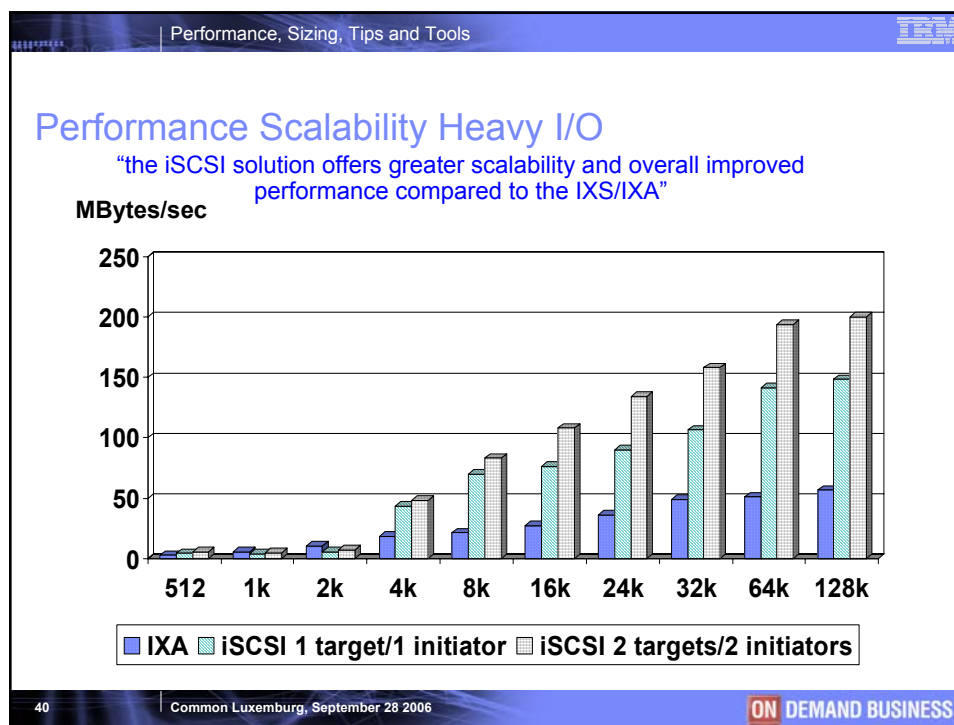
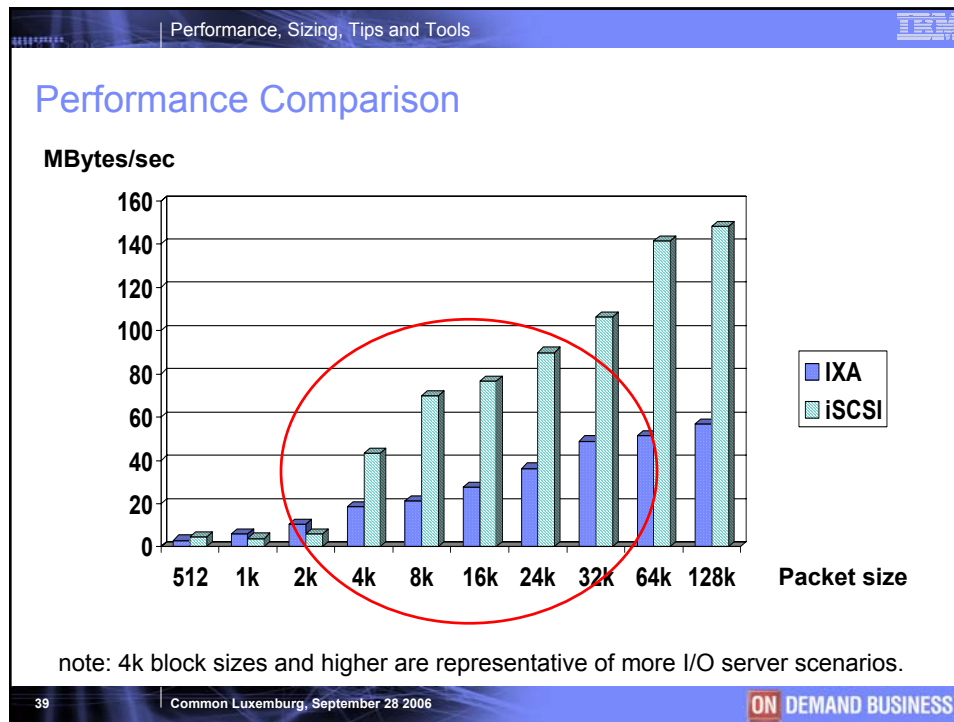
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iSCSI Support

- iSCSI support includes IXS and IXA capabilities:
 - All disks are virtual disks, enabling storage consolidation
 - Centralized server management
 - i5/OS tape, optical, and virtual Ethernet devices
- Extensive scalability range
 - Many System x-based servers through one iSCSI Host Bus Adapter (HBA) for a lower cost connectivity, up to a number of iSCSI HBAs per individual System x-based servers for scalable bandwidth
 - Each HBA can be shared by up to 8 integrated servers. In configurations with low to moderate virtual disk I/O rates this can result in a lower cost solution compared to an IXS or IXA option per integrated server.
- Capability to specify multiple virtual disk paths
- Lower incremental cost per integrated "server"
- Utilization of TCP/IP skills to install and configure
- Leveraging industry standards with iSCSI

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System i CPW Sizing

- V5R2 CPW - IXS/IXA
 - 10 CPW per 100 Disk Ops/sec
- V5R3 CPW - IXS/IXA
 - 12 CPW per 100 Disk Ops/sec
- V5R4 CPW
 - 13 CPW per 100 Disk Ops/sec - IXS/IXA
 - 19 CPW per 100 Disk Ops/sec - iSCSI

iSCSI example: Windows server doing 300 Ops/sec

- $CPW = 300 * 19/100$
- $CPW = 57$

Note: These rules of thumb are estimated from the results of performing file serving or application types of loads with various packet sizes. For more specific details see System i Capabilities Reference Guide

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System i Memory Sizing

Memory Pool Type	For Each Active Target HBA	For each Active NWSD
Machine Pool:	21 MBytes	1 MByte
Base Pool:**	1 MBytes	0.5 MByte
QFPHIS Private Pool	0.5 MByte	1 MByte *
Total	22.5 MBytes	2.5 MBytes

Example: 6 System x doing typical workload

- 2 Target HBAs
- 6 NWSDs
- Total Additional Memory = $500^{**} + (2 \times 22.5) + (6 \times 2.5) = 560$ MBytes

* Private pool assigned to QFPHIS must still be a 4 MB minimum size
 ** IBM Director Server requires an additional 500MBytes in the base pool

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System i iSCSI Target HBA Sizing

Each System i iSCSI Target HBA can do approximately 90 Mbytes/sec

- Based on approx 8K packet sizes
- Mixed workload of reads/writes

Collect data via Windows Performance monitor

- Physical Disk performance object
- Add up Disk Bytes/sec for all Windows server

Server	MB/sec
Server A	18.5
Server B	5
Server C	56.5
Server D	90
Server E	2
Total	172

- # targets = $172/85 = 2.02$
- # targets = 2 + 1 for redundancy

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General Rules of Thumb

- 3 System x or Blade servers per System i iSCSI Target HBA (recommend a 1 extra for redundancy)
- 70 CPW per System x or Blade server
- 500 MB + 20 MB per System x or Blade

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What's out there - Revisited?

- Applications and How to Make Them Behave
- Tools and Where They Are

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Java using 32-bit or 64-bit Virtual Machines

- I5/OS V5R4 delivers two JVM and SDK environments:
 - Java TM 2 Software Development Kit, Standard Edition
 - “Classic”, running in 64-bit mode
 - “32-bit” IBM Technology for JVM runs in 32-bit mode
- “32-bit” implements a standard IBM JVM 1.5 runtime environment and SDK:
 - Specifically focused for applications on smaller systems, with less impact on memory utilization
 - Simple implementation, mostly without code changes for the majority of the applications
 - Affects management of storage references
- Garbage Collection is similar to most other platforms for the 32-bit JVM, while it remains unchanged for the 64-bit JVM
- Requires less storage and heap size than the 64-bit version

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Java Performance: JIT versus DE

- Starting with V5R3, JIT is always recommended for best performance of 32-bit (no DE) or 64-bit
- Direct Execution (DE) is the default, unless:
 - ADDENVVAR ENVVAR (JAVA_COMPILER) VALUE(jitc) JAVA CLASS(Test)
 - OR Create your Java programs under:
 - Either Set java.compiler system property on the i5/OS command line -> JAVA CLASS(Test) PROP((java.compiler jitc)) or
 - Set the java.compiler system property on the Qshell Interpreter command line. For example, enter java -Djava.compiler=jitc Test
- Even if running JIT, the JVM will have to create a Java program object (with optimization level *INTERPRET) the first time a particular Java class is used on the system, if one does not already exist.
 - Creation of this program object is much faster than creating a full DE program, but it may still take some time
 - Running CRTJVAPGM with OPTIMIZE(*INTERPRET) will create this program ahead of time, making the first startup faster

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Analyzing JVM – Memory Needs

- To determine the approximate amount of memory required for a single JVM:
 - Start the JVM.
 - Ramp up the workload on the JVM.
 - Perform the i5/OS Dump JVM (DMPJVM) command on the JVM and look at the output section labeled Garbage Collection.
 - The general rule is that this JVM will require the amount of memory that is indicated by the following formula (where JIT stands for just in time): $\text{heap size} + ((\text{JIT heap size} + \text{JVM heap size})/2) \Rightarrow 1887964 + ((272352 + 598204)/2) = 2.278.242 \text{ KB}$

```

.....
Garbage Collection
.....
Garbage collector parameters
Initial size: 786432 K
Max size: 240000000 K
Current values
Heap size: 1887964 K
Garbage collections: 42
Additional values
JIT heap size: 272352 K
JVM heap size: 598204 K

```

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Database Optimization

- Index structure:
 - Organize indexes to be created from *most complex* to *least complex*
 - Create indexes with access path size set to 1 TB
 - Consider the use of encoded vector indexes for fast retrieval if low cardinality of the key
 - Create the Perfect Index (become eligible for a Grand Prize!)
- Enable the Optimizer to make wise decisions:
 - Check if Statistics are being collected correctly
- Perform pro-active query tuning
- Consider Materialized Query Tables
- Use Symmetric Processing where possible
 - Fine-grained control (use properties file QAQQINI) versus coarse-grained control (using system values)
- Re-use deleted record space

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What's out there - Revisited?

- Applications and How to Make Them Behave
- Tools and Where They Are

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The Tools for Analysis

- Included in i5/OS:
 - Work with Active Jobs (WRKACTJOB) - Work with System Status (WRKSYSSTS) - Work with Disk Status (WRKDSKSTS)
 - Collection Services (STRPFCOL)
 - Performance Trace (STRPFTRC)
 - SQL Performance Monitors (STRDBMON)
 - iSeries Navigator equivalents and Monitors
- Included in the Performance Tools Licensed Product:
 - Work with System Activity (WRKSYSACT)
 - Performance Explorer
 - Reports for Collection and Trace functions
 - Performance Advisor
- Advanced Tooling:
 - iDoctor, Job Watcher, PEX Trace Data Visualizer
 - Web Performance Monitor and Advisor

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Job Watcher Sample

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PEX Trace Data Visualizer

Job/Thread ID	Caused Events	Total events	Cum CPU Time (ns)	Active time (ns)
All Jobs	Yes	1,756	9,280,754,000	802,596,267,096,000
Event causing Jobs	Yes	1,756	8,834,324,000	501,928,186,776,000
WEBSPPHERE_QJEJBSVR/079002	Yes	560	3,002,916,000	59,144,353,032,000
WEBSPPHERE_QJEJBSVR/077872	Yes	258	1,244,402,000	49,975,301,040,000
SHANTIQJEJBSVR/078663	Yes	163	786,256,000	35,452,503,988,000
SERVER/QNOTES/079570	Yes	95	533,322,000	29,831,263,042,000
EVENT/QNOTES/079572	Yes	89	461,250,000	5,495,232,658,000
HTTP/QNOTES/079575	Yes	88	415,066,000	22,373,447,280,000
QJVAEXEC/QTCP/077784	Yes	84	410,598,000	6,217,098,082,000
QCPMGTSVR/QCPMGDIR/077770	Yes	83	350,158,000	41,295,604,354,000
ADMINQJEJBSVR/077657	Yes	34	150,710,000	16,179,527,818,000

PEX data loaded

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Performance, Sizing, Tips and Tools

The Tools for Capacity Planning and Sizing

- Workload Estimator
 - See: <http://www-912.ibm.com/estimator/index.html>
- Performance Navigator
 - See: <http://www.mpginc.com/>
- BMC PATROL for iSeries – Predict
 - See: http://www.bmc.com/products/proddocview/0_2832_19052_19429_4091705_9687_0_0.html

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